

# 135  
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XA-9627  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Naoki YADA et al.

Appln. No.: 10/080,681

Group Art Unit: 2824

Filed: February 25, 2002

For: MICROCOMPUTER, PROGRAMMING METHOD AND ERASING METHOD

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

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Sir:

Technology Center 2100

Pursuant to 37 C.F.R. § 1.56, and without any  
assertion as to materiality or prior art effect, the  
document listed on the attached Form PTO-1449 is hereby  
cited.

The document on the attached List is cited in the  
specification, on page 2, and its relevance is indicated  
therein.

Respectfully submitted,

MWS:sjk

Miles & Stockbridge P.C.  
1751 Pinnacle Drive  
Suite 500  
McLean, Virginia 22102-3833  
(703) 903-9000

April 26, 2002

By:

*Mitchell W. Shapiro*  
Mitchell W. Shapiro  
Reg. No. 31,568

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